



Information Disclosure Statement

USSN 10/749,575

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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-3932 617882-7	U.S. SERIAL NO. 10/749,575
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT(S) Peter CAPOFREDDI	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
<i>PP</i>	6,005,505	12/1999	Linz	341	143	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>PP</i>	Adams, R., et al., "A 113-dB SNR Oversampling DAC with Segmented Noise-Shaped Scrambling," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, No. 12, pp. 1871-1878 (December 1998).	
<i>PP</i>	Adams, R., et al., "Stability Theory for $\Delta\Sigma$ Modulators," <i>Delta-Sigma Data Converters</i> , IEEE Press, New Jersey, pp. 141-152 (1997)	
<i>PP</i>	Proakis, J.G., et al., <i>Communication Systems Engineering</i> , Prentice Hall, New Jersey, pp. 276-282 (1994).	
<i>PP</i>	Rugh, W., <i>Nonlinear System Theory: The Volterra/Wiener Approach</i> , Johns Hopkins University Press, London, pp. 253-255 (1981).	
<i>PP</i>	Su, D.K., et al., "A CMOS Oversampling D/A Converter with a Current-Mode Semi-Digital Reconstruction Filter," <i>IEEE International Solid-State Circuits Conference</i> , pp. 230-231 (1993)	
<i>PP</i>	Weiner, D., et al., <i>Sinusoidal Analysis and Modeling of Weakly Nonlinear Circuits with Application to Nonlinear Interference Effects</i> , Van Nostrand Reinhold Company, New York, pp. 82-84 (1980)	

EXAMINER	DATE CONSIDERED
<i>Pherry Phu</i>	<i>5/31/07</i>

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.